



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Fee et al.

**Serial No.:** 09/933,297

**Filed:** August 20, 2001

**For:** QUAD FLAT NO-LEAD (QFN) GRID  
ARRAY PACKAGE, METHOD OF  
MAKING AND MEMORY MODULE AND  
COMPUTER SYSTEM

**Confirmation No.:** 5686

**Examiner:** D. Graybill

**Group Art Unit:** 2822

**Attorney Docket No.:** 2269-4738US  
(00-1113.00/US)

**Notice of Allowance Mailed:**

June 2, 2005

**NOTICE OF EXPRESS MAILING**

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Date of Deposit with USPS: September 2, 2005

Person making Deposit: Steve Wong

**AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

**Amendments to the Specification** begin on page 3 of this paper.

**Amendments to the Abstract** appear on page 10 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 11 of this paper.

**Amendments to the Drawings** appear on page 17 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

**Remarks** begin on page 18 of this paper.

An **Appendix** including amended drawing figures is attached following page 18 of this paper.